

100

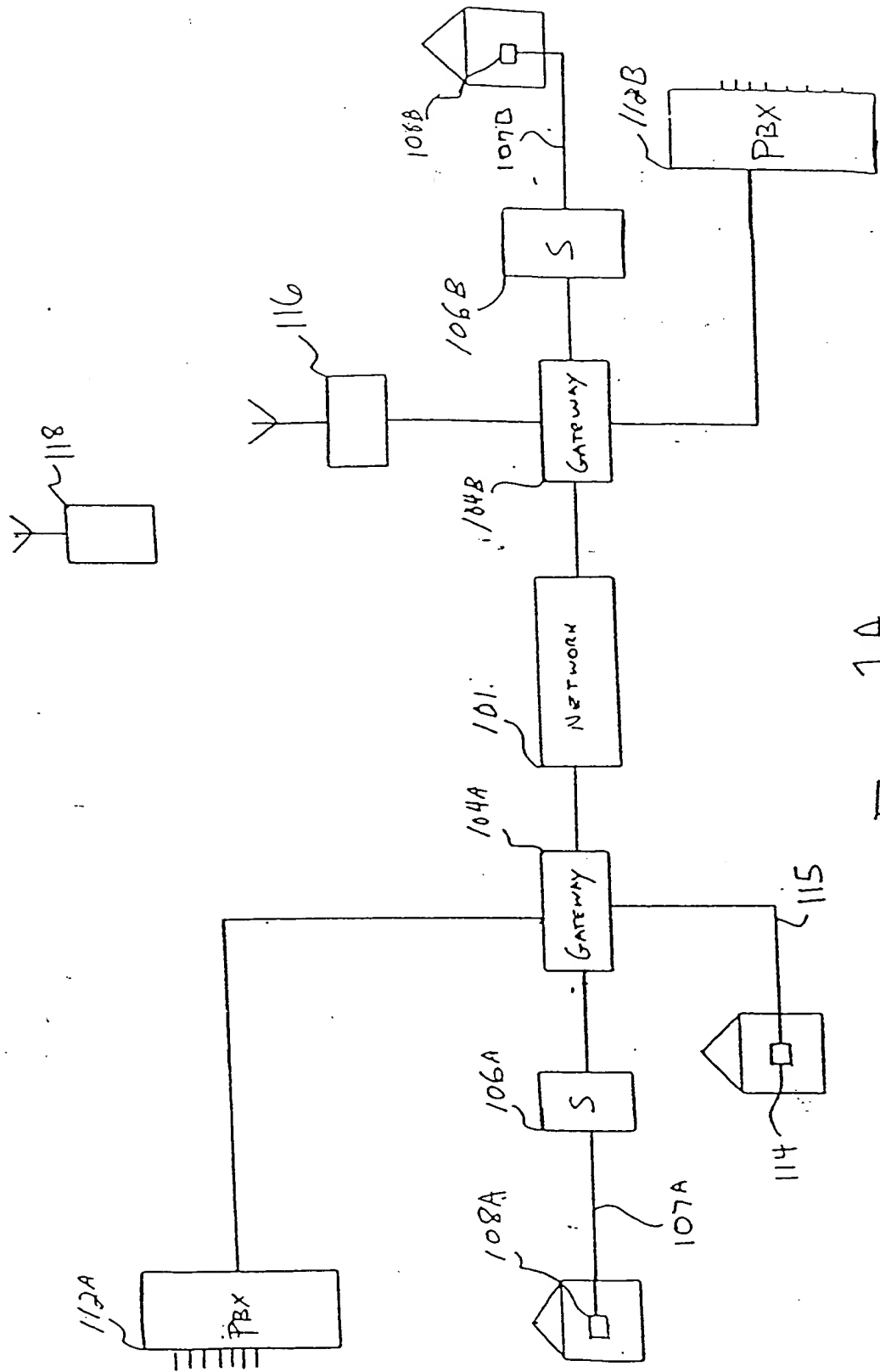


FIG. 1A

150

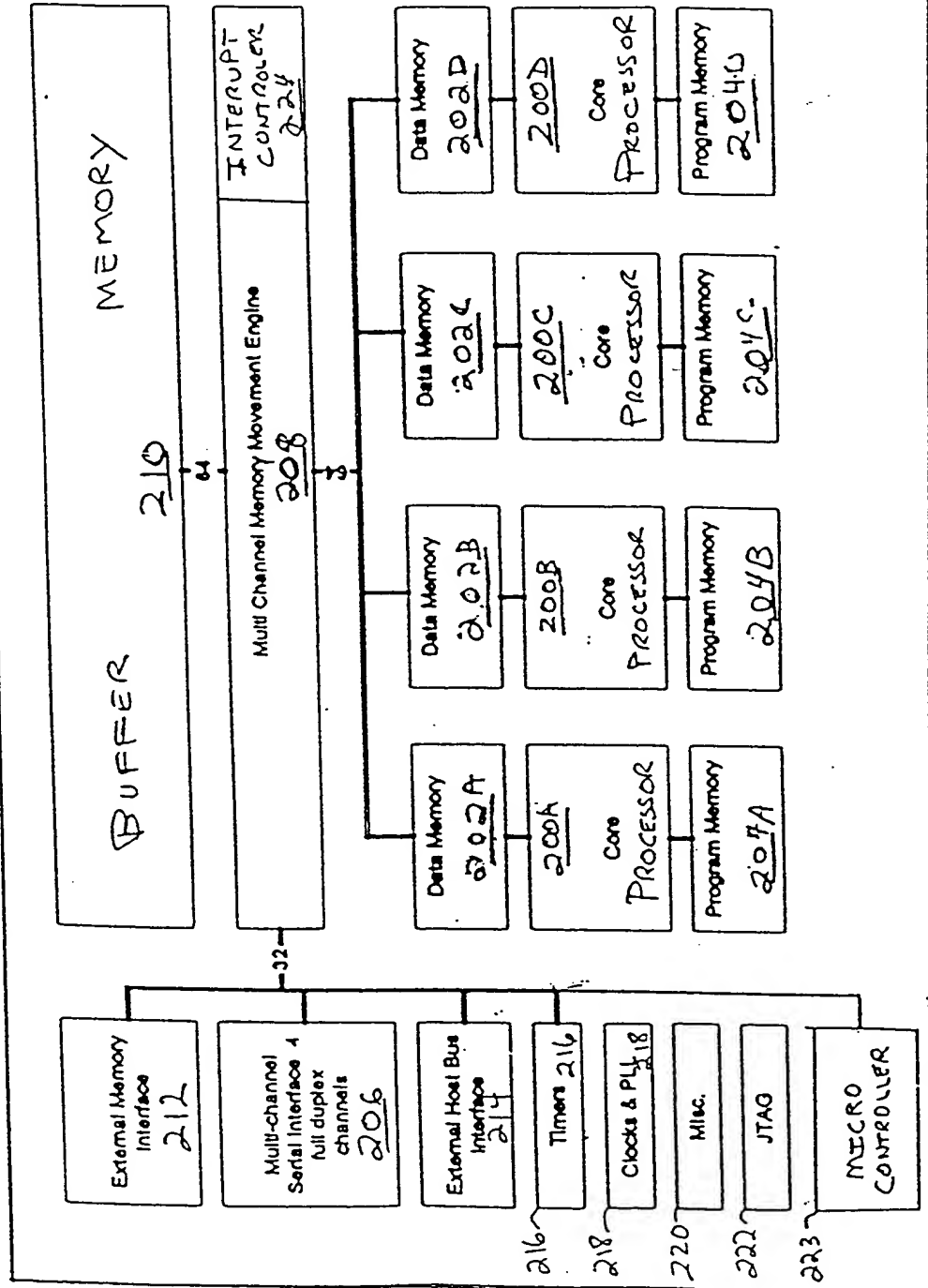


FIG. 2

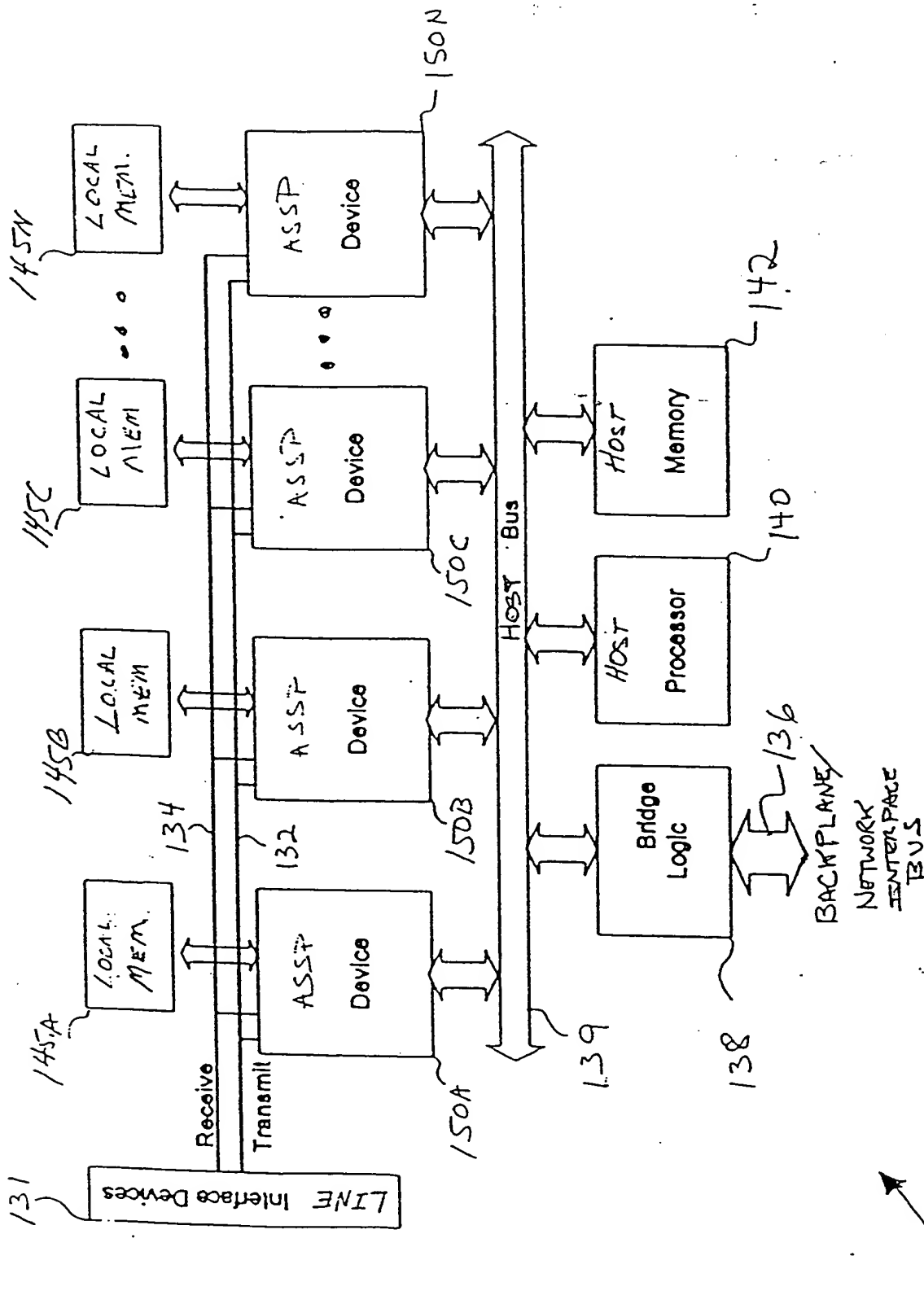


FIG. 1B

1. A system for providing a network interface to a host system, comprising:
 a. a network interface device (131) for receiving data from a network;
 b. a host system (140) for processing the data;
 c. a host bus (139) for connecting the network interface device to the host system;
 d. a bridge logic (138) for managing data flow between the network interface device and the host system;
 e. a backplane/network interface bus (136) for connecting the bridge logic to the host system.

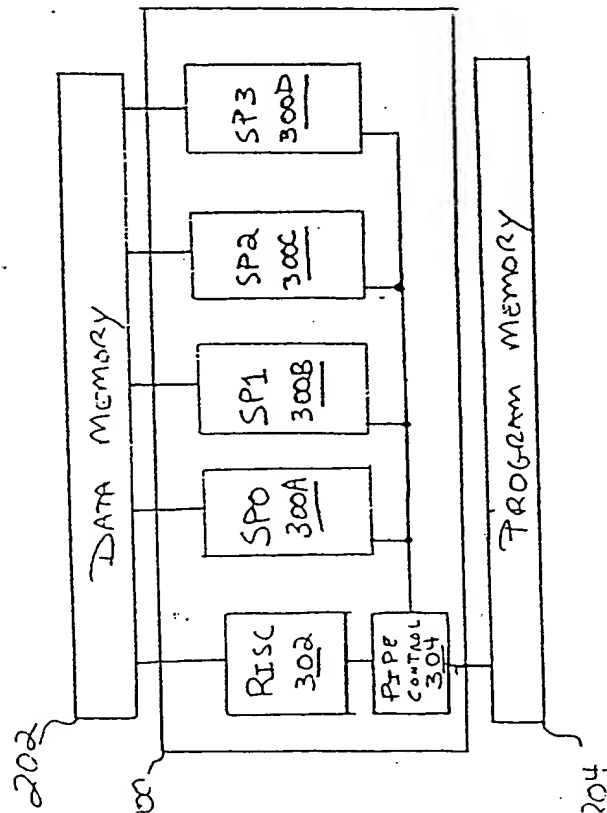


FIG. 3

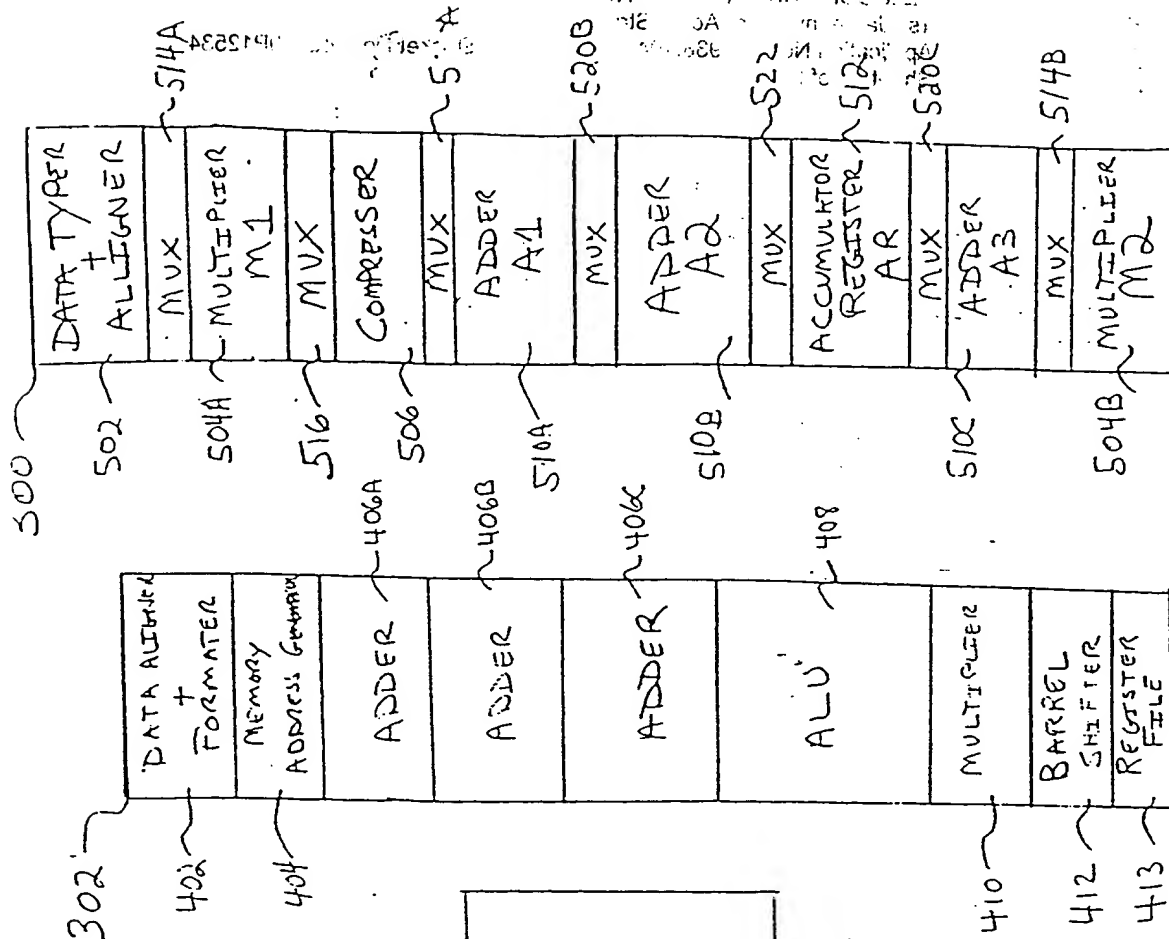


FIG. 4

FIG. 5A

$\begin{array}{c} \text{---} X \quad Y \quad \sqrt{Z} \\ \text{531} \quad \downarrow \quad \uparrow \quad \text{532} \\ \text{533} \end{array}$

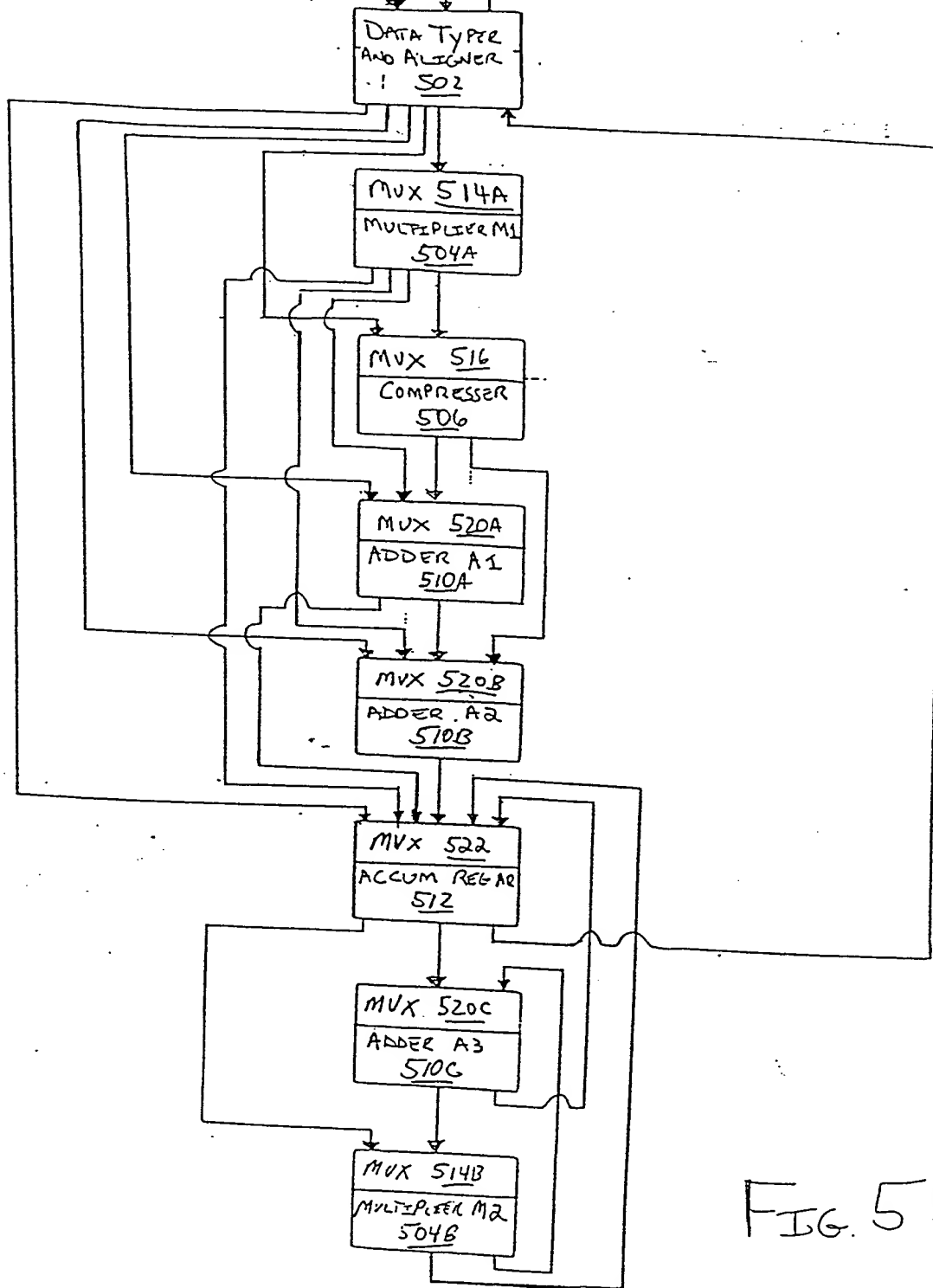
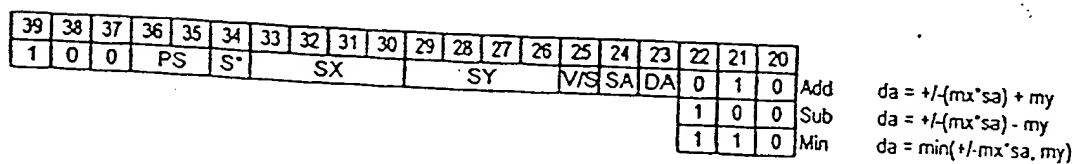
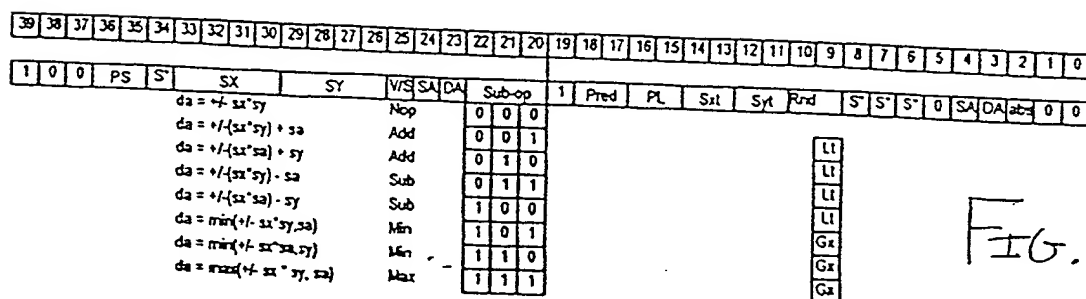
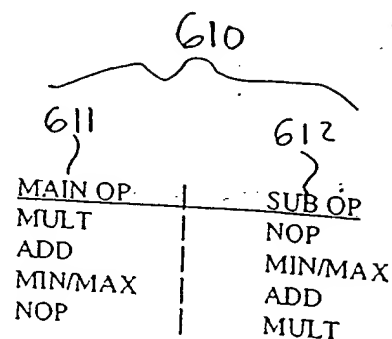
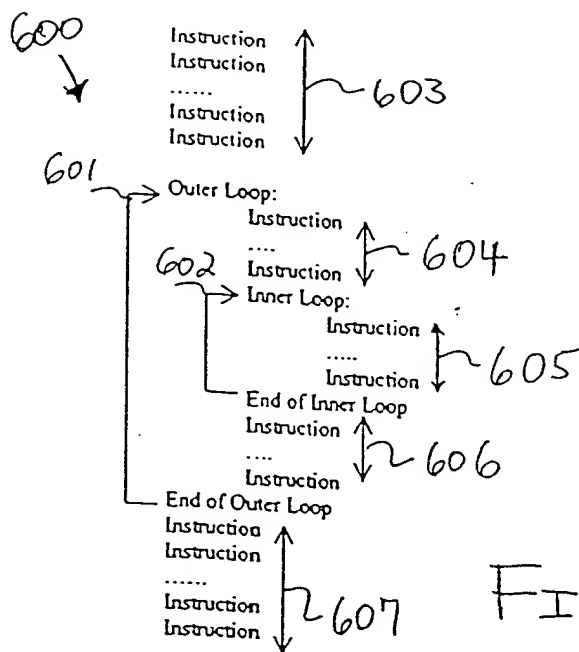


FIG. 5B



Control II Control
Control # Control
DSP, extensions/Shadow
DSP # DSP

19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39
----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

[illegible]

SX		SY	x	x	x	1	1	1
SX		Type	x	areq		1	1	1
SX								
SX		SY		SA DA V/S			Sub-op	

10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----

0	Pred	PL	Sst	Syl	And	Ll	Gx			S*	S*	S*	0	SA	DΔ	abj	0	0
---	------	----	-----	-----	-----	----	----	--	--	----	----	----	---	----	----	-----	---	---

0	P _{rad}	P _L	S _{x1}	S _{y1}	L ₁	Sub-set	0	S _A	D _A	D ₀₁	0	0
							v ₁	v ₁	v ₁	x		
							x	VIS	And	Fp		
							tr-cil	Gx	Fp			

0	Prod	PL	Sst	Syl	U-CH	Gz	Sub-ent	0	SA	DA	DOI	0	0
<div> <div>LI</div> <div>Fp</div> </div>													
<div> <div>Rnd</div> <div>V/S</div> </div>													

0	Pred	PL	S ₁₁	P _{CU1}	0	0	0	0
---	------	----	-----------------	------------------	---	---	---	---

19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

Q	Pred	PL	κ	Type: SX	Type: SY	0 SA	DA	π	0 1
0	Pred	PL		Permute: SX	Permute: SY	0 SA	DA	π	1 0
1	Pred	PL		Permute: SX	Permute: SY	0 SA	DA	π	1 1

0	Op	Pt	Op	ereo	ereo	1	SA	DA	Sub-op										
19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

FIG. 6E

Control Instructions

	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
add.sub	L	Pred	0	0	0	0	RX				RY					RZ			*/	0
max.min	L	Pred	0	0	0	0	RX				RY					RZ			X/N	1
Shift	L	Pred	0	0	0	0	RX				RY					RZ			U1	RA
Logic	L	Pred	0	0	0	0	RX				RY					RZ			Δ	Δ
Mux	L	Pred	0	0	0	0	RX				RY					RZ			Pd	0
mov	L	Pred	0	0	0	0	RX				RY					RZ			0	0
addi	L	Pred	0	0	0	0	RX				RY					RZ			0	0
mov2reg	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Ldm	L	Pred	0	0	0	0	RX				RY					RZ			0	0
stbts	L	Pred	0	0	0	0	RX				RY					RZ			0	0
bits	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Setbit	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Nov4	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Jump	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Call	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Loop	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Jmpi	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Call	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Loopl	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Test	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Testbit	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Andp,orp	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Load	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Store	L	Pred	0	0	0	0	RX				RY					RZ			0	0
eLoad	L	Pred	0	0	0	0	RX				RY					RZ			0	0
eStore	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Extends	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Logic2	L	Pred	0	0	0	0	RX				RY					RZ			0	0
mov-arg	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Cm	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Parity	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Sum	L	Pred	0	0	0	0	RX				RY					RZ			0	0
As	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Nag	L	Pred	0	0	0	0	RX				RY					RZ			0	0
hw-itep	L	Pred	0	0	0	0	RX				RY					RZ			0	0
& Set	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Return	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Zero-ac	L	Pred	0	0	0	0	RX				RY					RZ			0	0
eSync	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Sw	L	Pred	0	0	0	0	RX				RY					RZ			0	0
Nop	L	Pred	0	0	0	0	RX				RY					RZ			0	0

<Blt1, Blt9-0> == U15 (Shift Amount)

<Blt1, Blt13-10> == U15:POS

FIG. 6 F

Checked by: 06/08/04
 Approved by: 06/08/04
 TELECOMMUNICATIONS IS PROCESSING
 TIME: 14:00
 BAKER, J. & S. (14/08/04)
 (14/08/04)

Group	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Period	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Periods	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Subgroup	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
PL	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
PS	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
VS	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
OA	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	7																											

PL	PS	Rnd	S'	DA	V/S	U	S'	S'	S'
	PS	Rnd	S'	DA <td>V/S</td> <td>U</td> <td>S'</td> <td></td> <td></td>	V/S	U	S'		
PL	Gx	S'	Rnd	SA <td>DA <td>V/S</td> <td>U</td> <td>S'</td> <td>N/A</td> </td>	DA <td>V/S</td> <td>U</td> <td>S'</td> <td>N/A</td>	V/S	U	S'	N/A
PL	OC	Rnd	SA <td>DA <td>V/S</td> <td>U</td> <td>S'</td> <td>S'</td> <td>SA</td> </td>	DA <td>V/S</td> <td>U</td> <td>S'</td> <td>S'</td> <td>SA</td>	V/S	U	S'	S'	SA
PL	OC	Rnd	SA <td>DA <td>V/S</td> <td>U</td> <td>S'</td> <td>S'</td> <td>SA</td> </td>	DA <td>V/S</td> <td>U</td> <td>S'</td> <td>S'</td> <td>SA</td>	V/S	U	S'	S'	SA

[illegible]

Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	Pred	32	33	34	35	36	37	38																																																														

[illegible][illegible][illegible]

Group	Prad				opadga				SA				SY				DPT				Subop			
	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
1	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
2	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
3	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
4	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
5	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
6	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
7	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
8	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
9	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
10	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
11	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
12	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
13	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
14	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
15	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
16	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
17	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
18	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
19	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
20	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
21	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	
22																								

[illegible]

Sheet 3 of 32
Application No. 10918
Faintive in English and Spanish
TELECOMMUNICATIONS PROCESSING
THE VOICE ACTIVITY DETECTOR FOR INTEGRATED
Bisely, Sebastian, Taylor & Zaiman LLP
(714) 887-3841

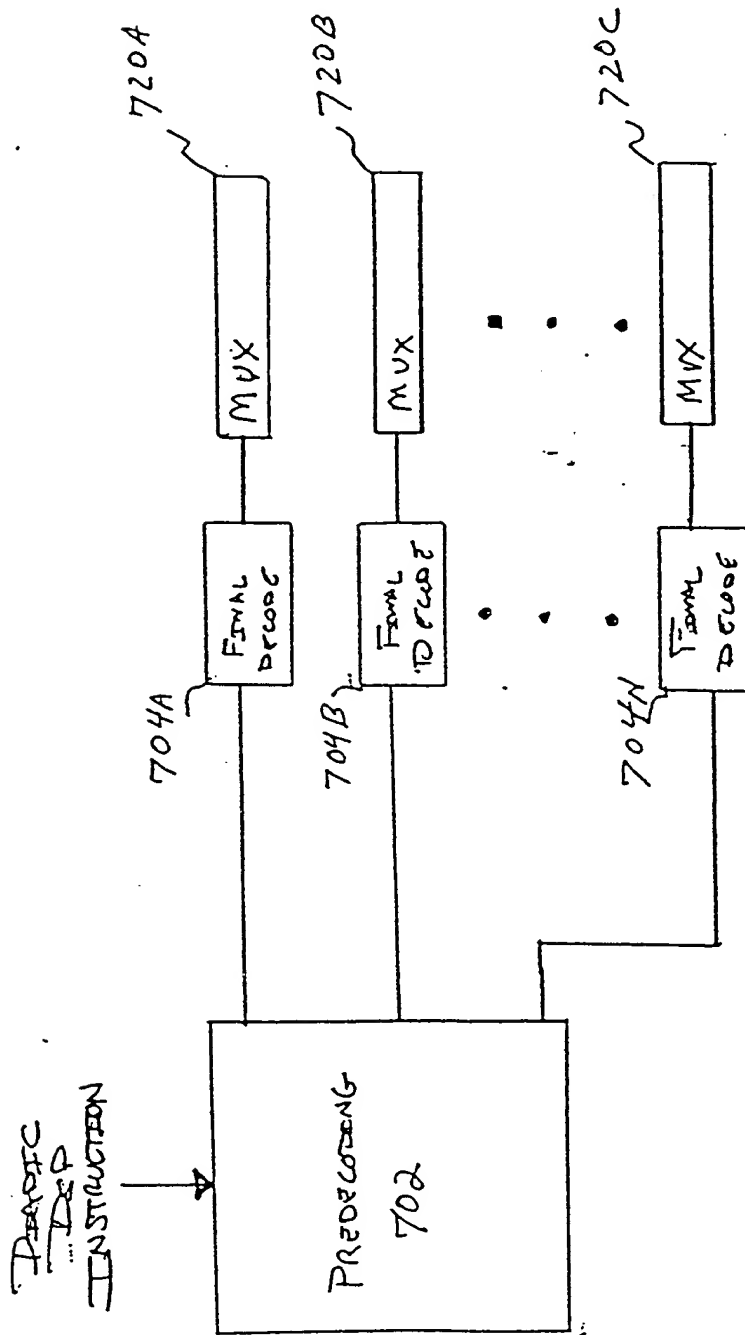
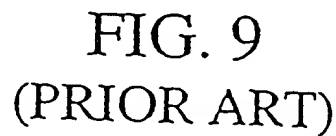
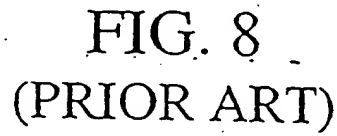


FIG. 7



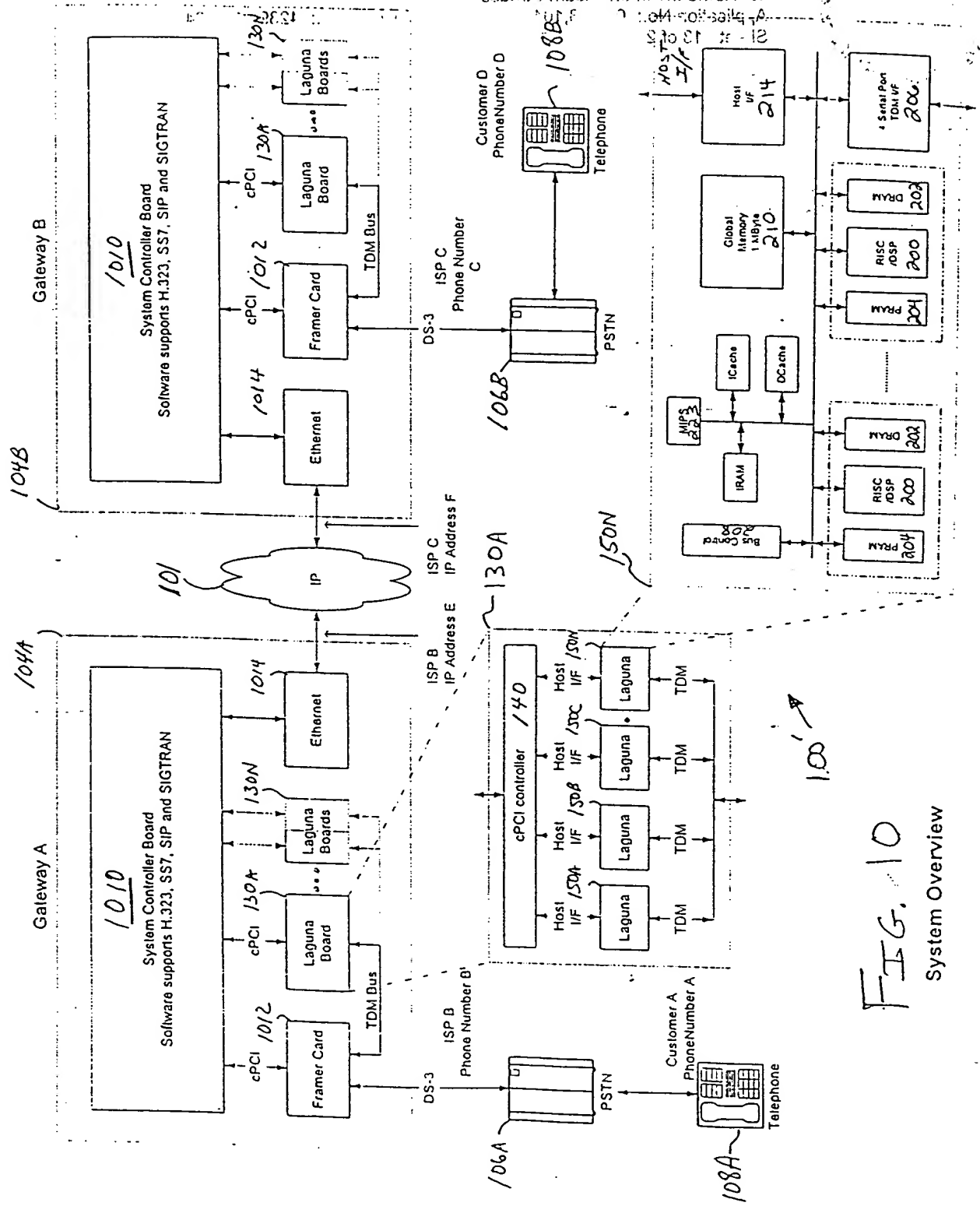


FIG. 10

System Overview

TELECOMMUNICATIONS PROCESSING
IN A VOICE ACTIVITY DETECTOR FOR INTEGRATED
TELEPHONE NETWORKS
Inventors: Adam Stans, et al.
Attorney: Skoloff, Taylor & Zisman LLP
(714) 221-7000

FIG. 10

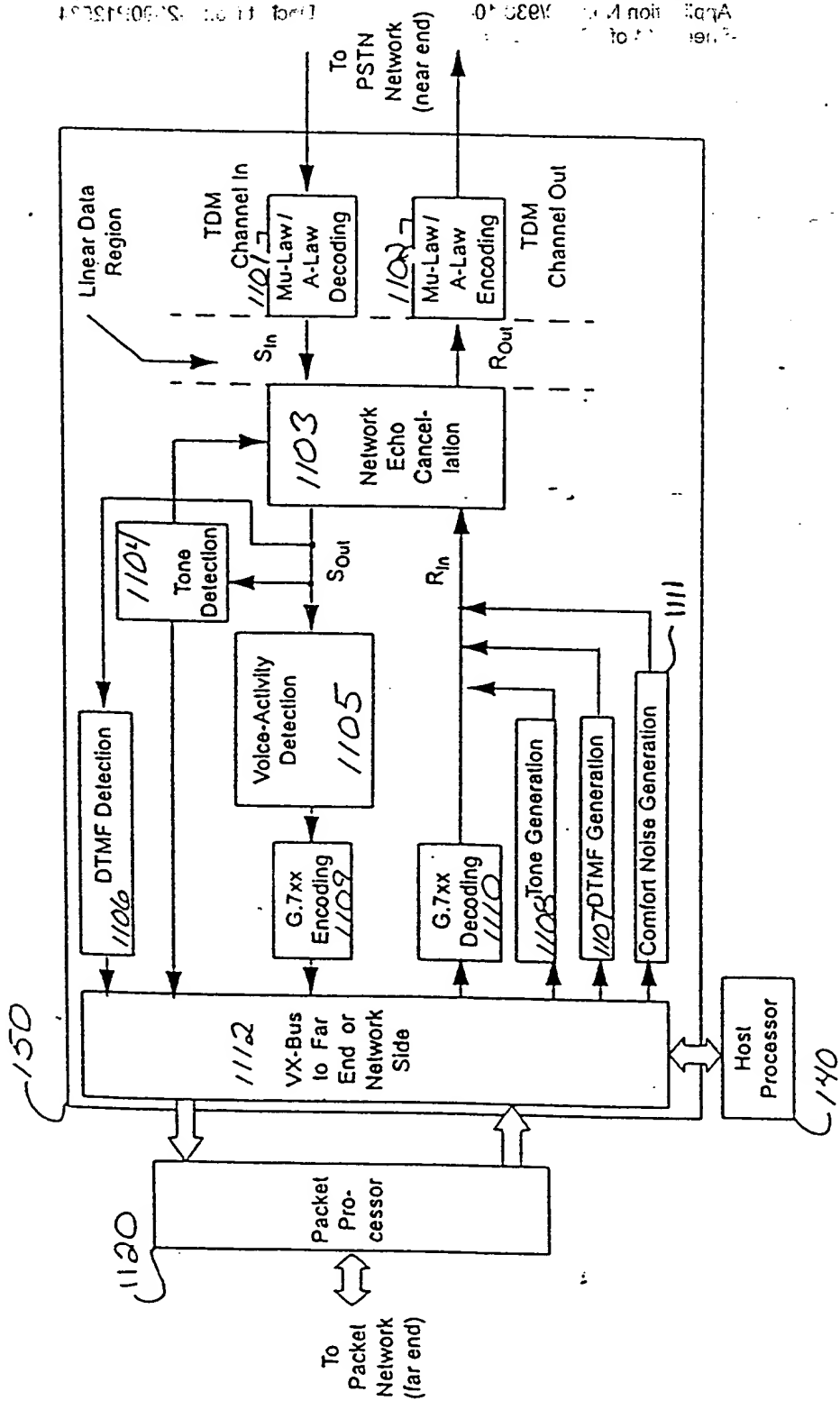


FIG. 11

Application No. 08/330,400
Filed 11/01/95
Inventors: David A. Smith, et al.
TELECOMMUNICATIONS FOR VOICE ACTIVITY DETECTION FOR IP
BRIEF SUMMARY OF THE INVENTION
This invention relates to a system and method for detecting voice activity in a packet network.

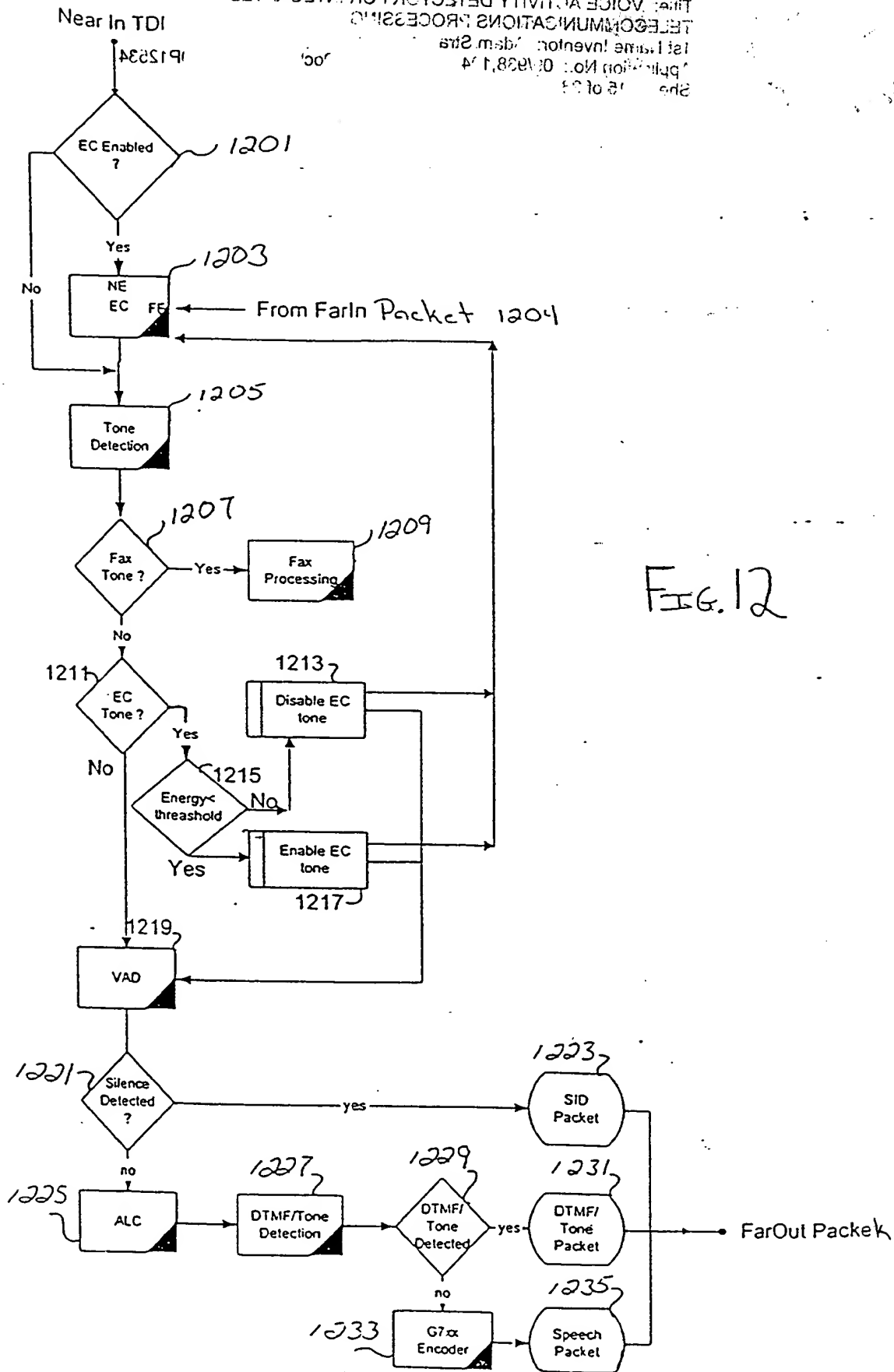


FIG. 12

FIG. 12

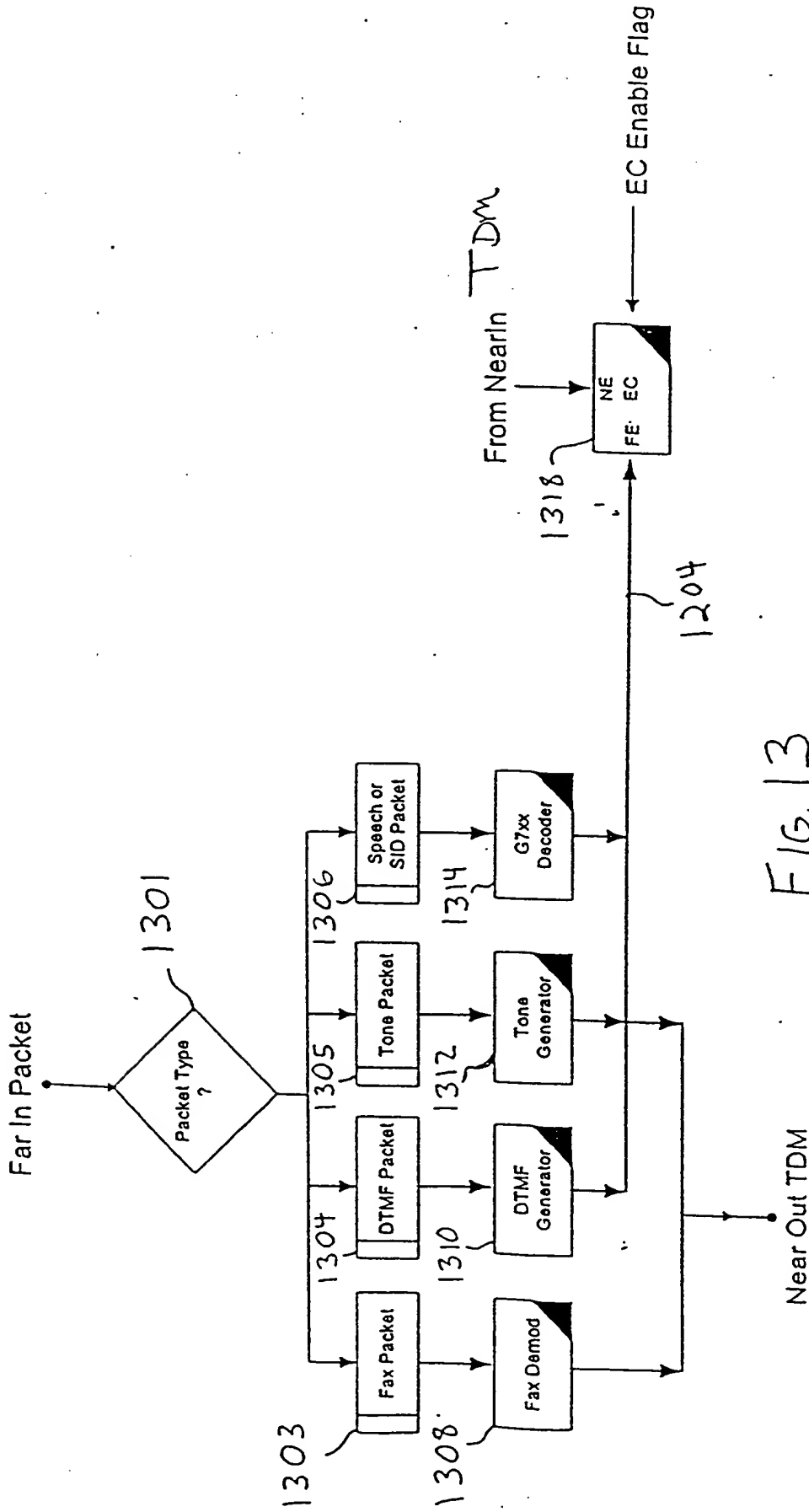


FIG. 13

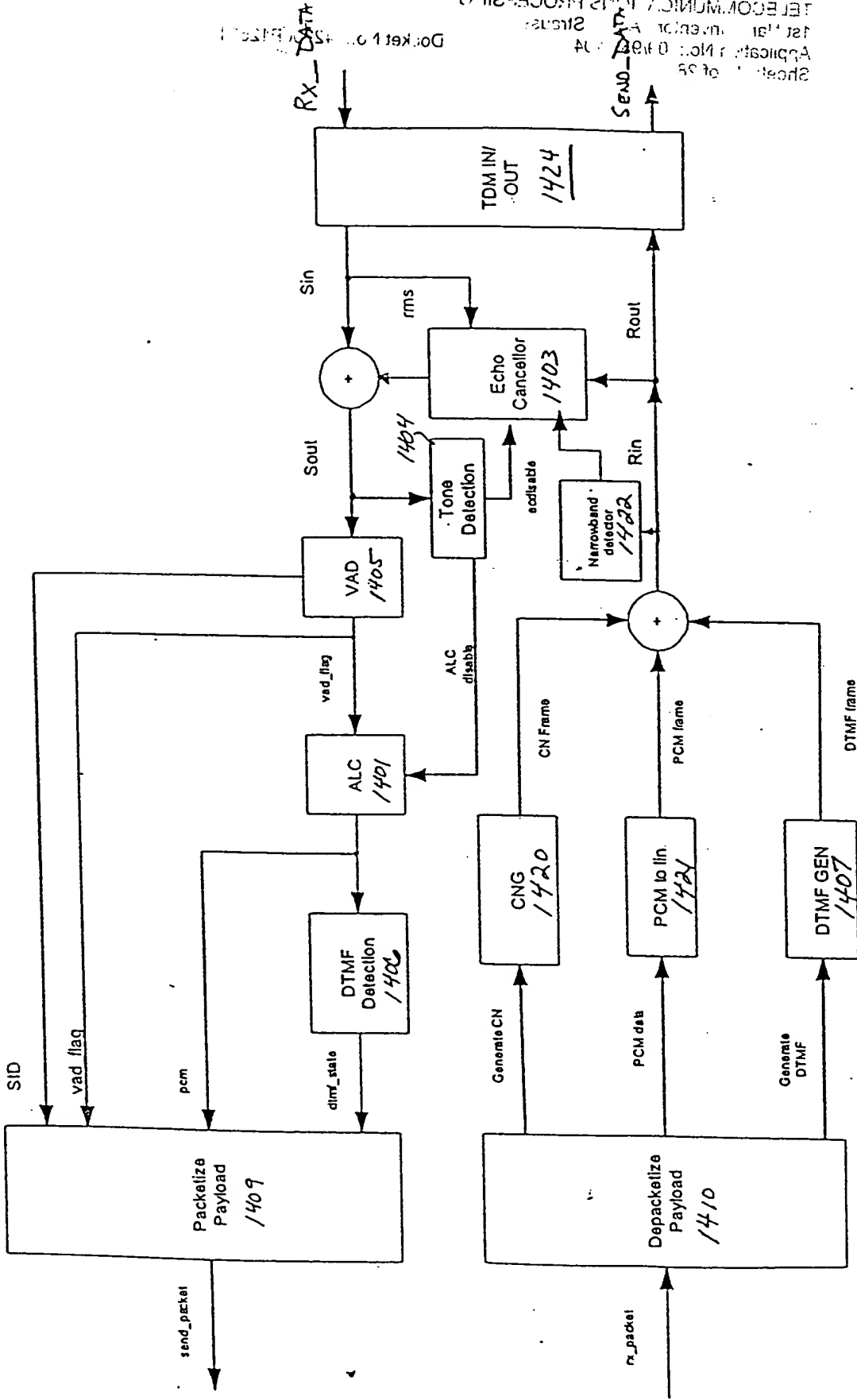


FIG. 14A

Sheet 1 of 2
Applicant: Motorola, Inc.
Attorney: Motorola, Inc.
First Invention: 1994
Title: TELECOMMUNICATIONS PROCESSING
THE VOICE ACTIVITY DETECTOR FOR THE
TELECOMMUNICATIONS PROCESSING
TAYLOR & ZETMAN LLP
(214) 551-1000

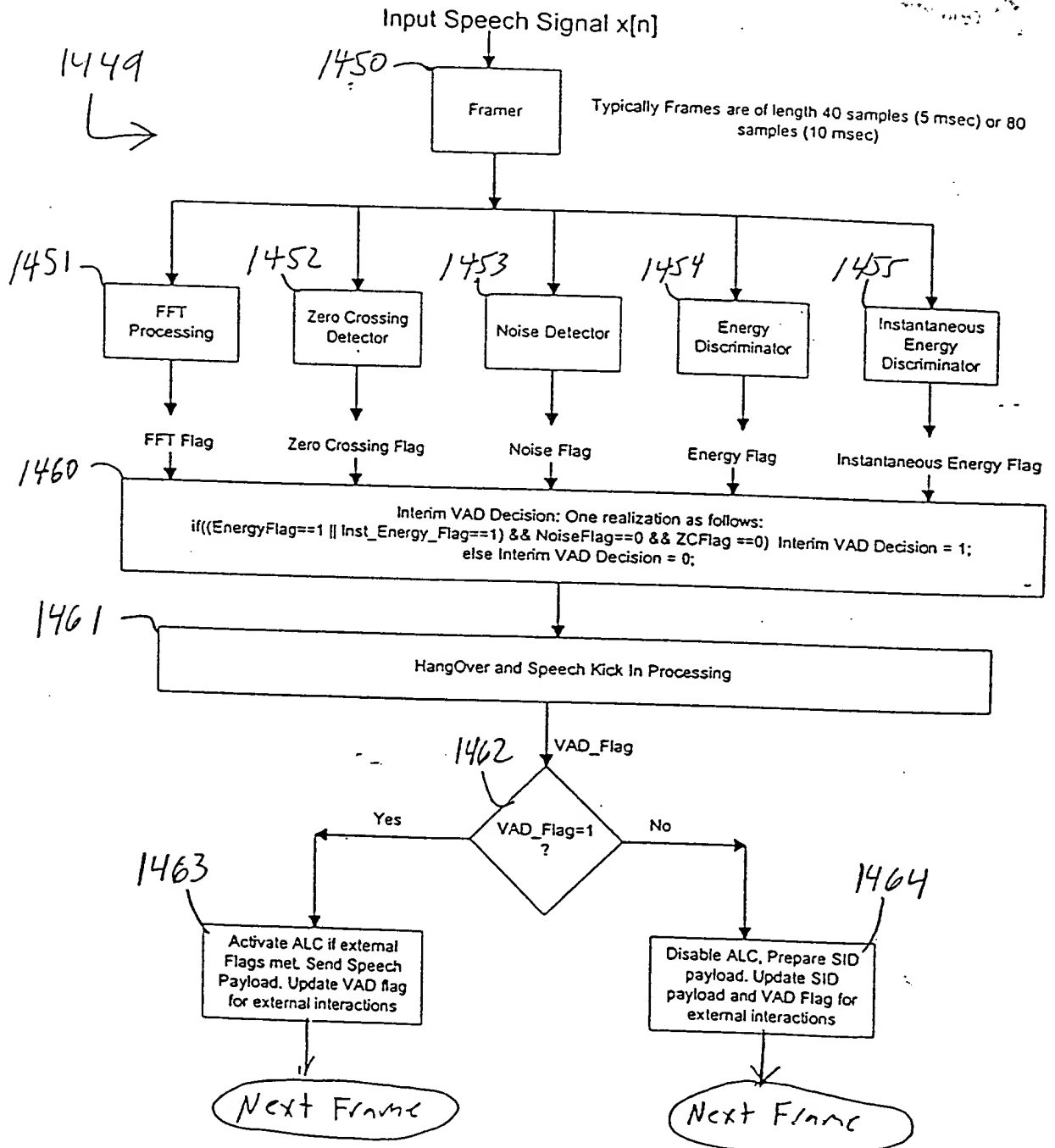
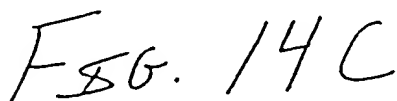


FIG. 14B

1451



Zero Crossing Detector

1452

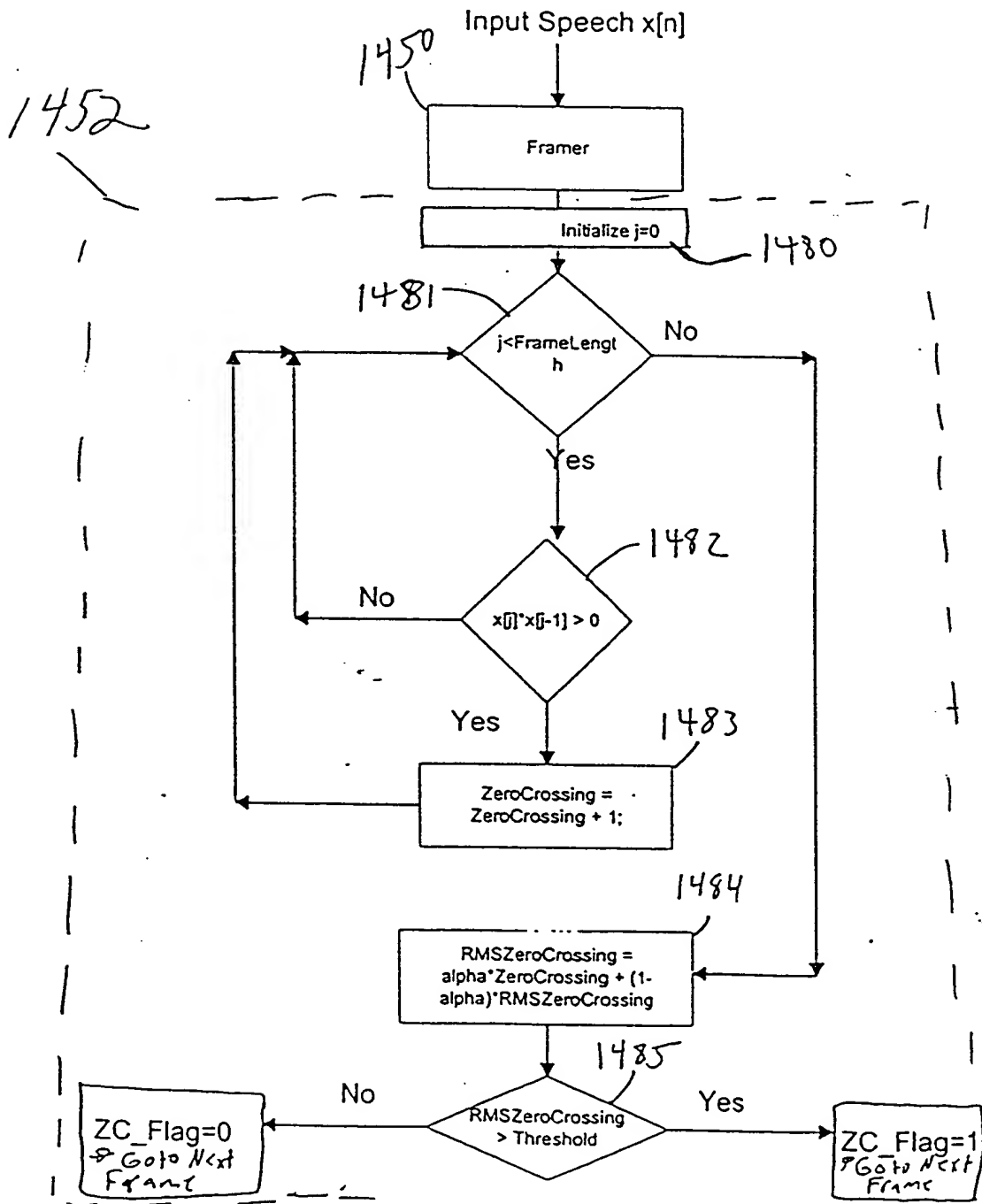
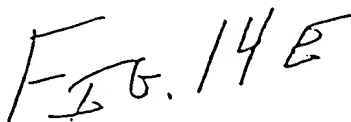


FIG. 14D



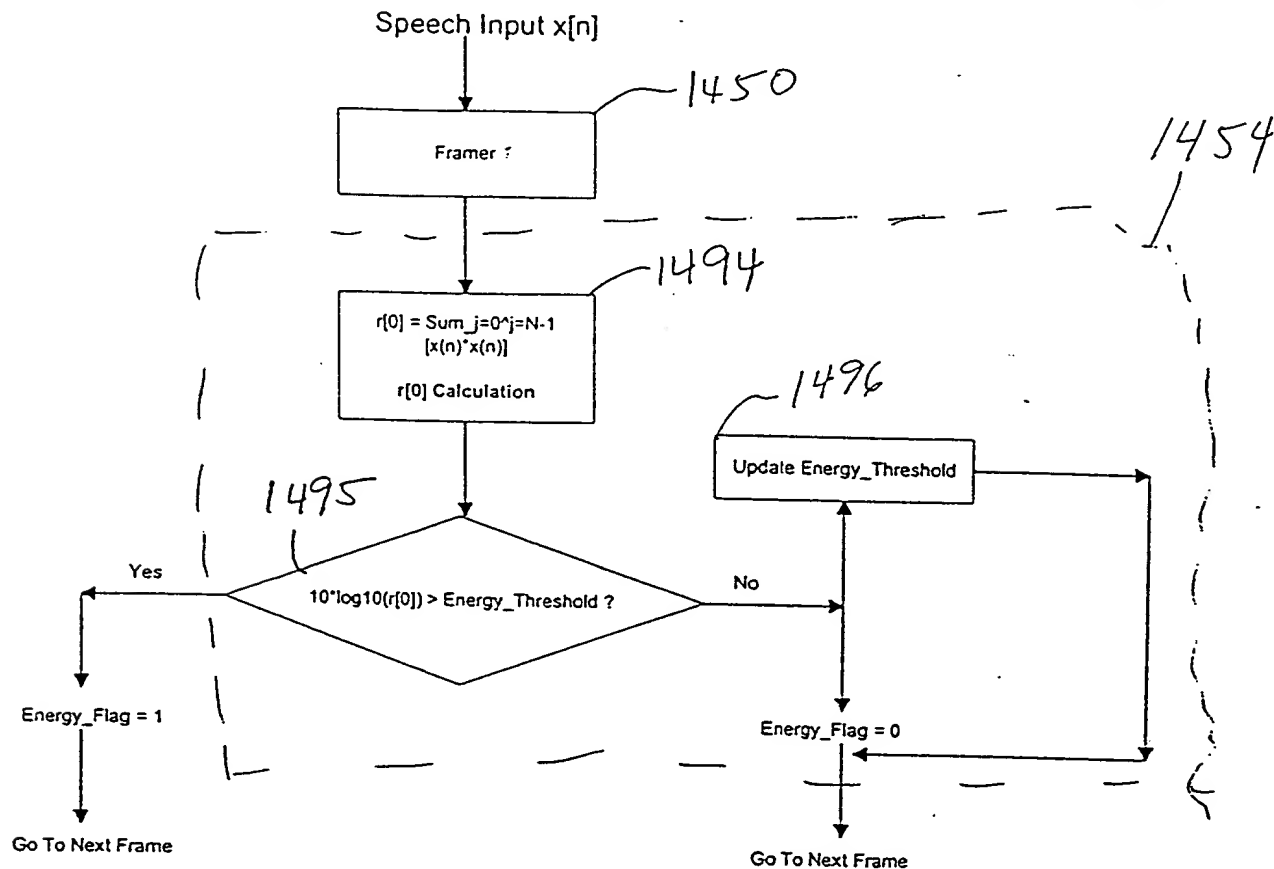
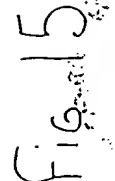


FIG. 14F



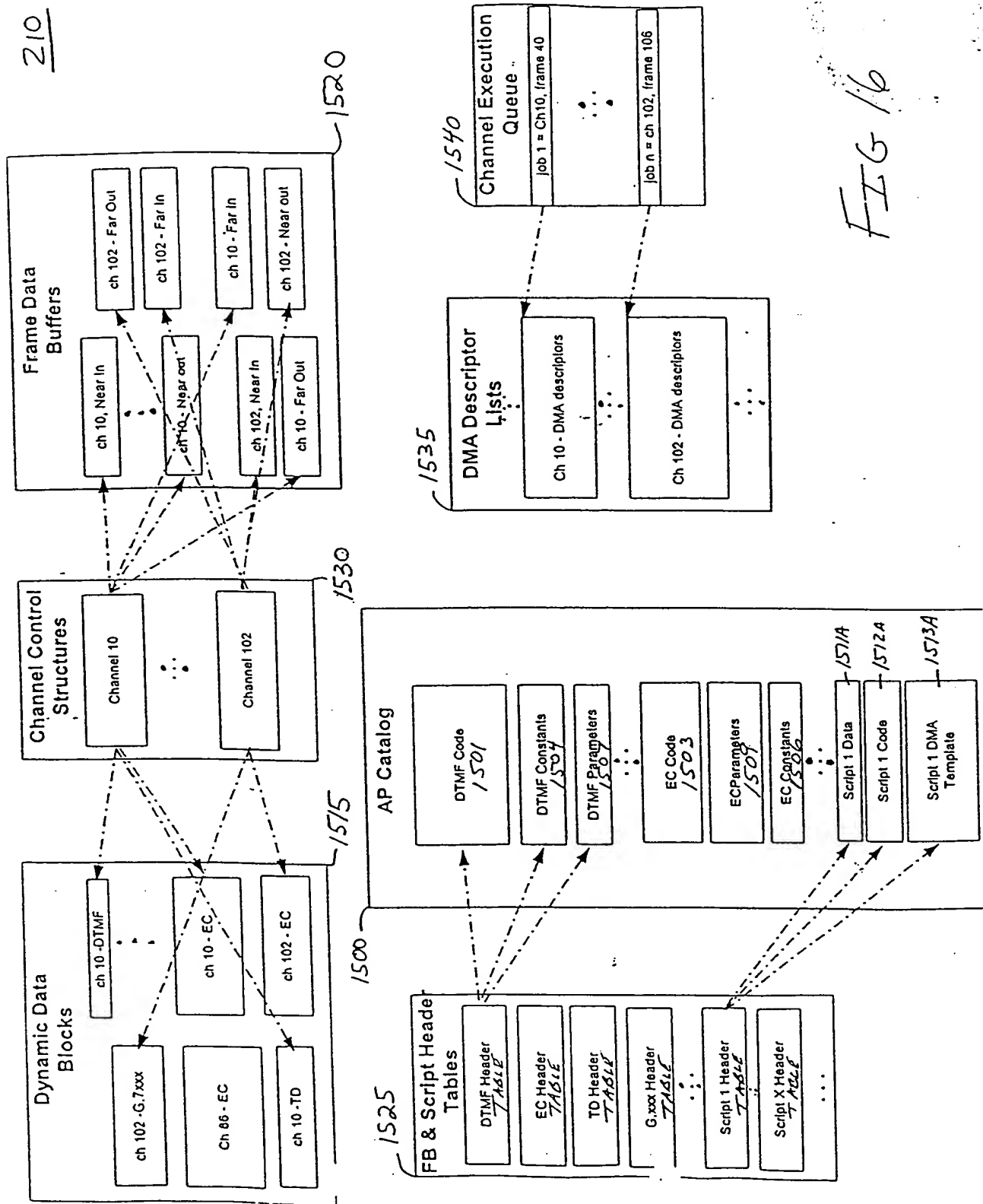


FIG. 17

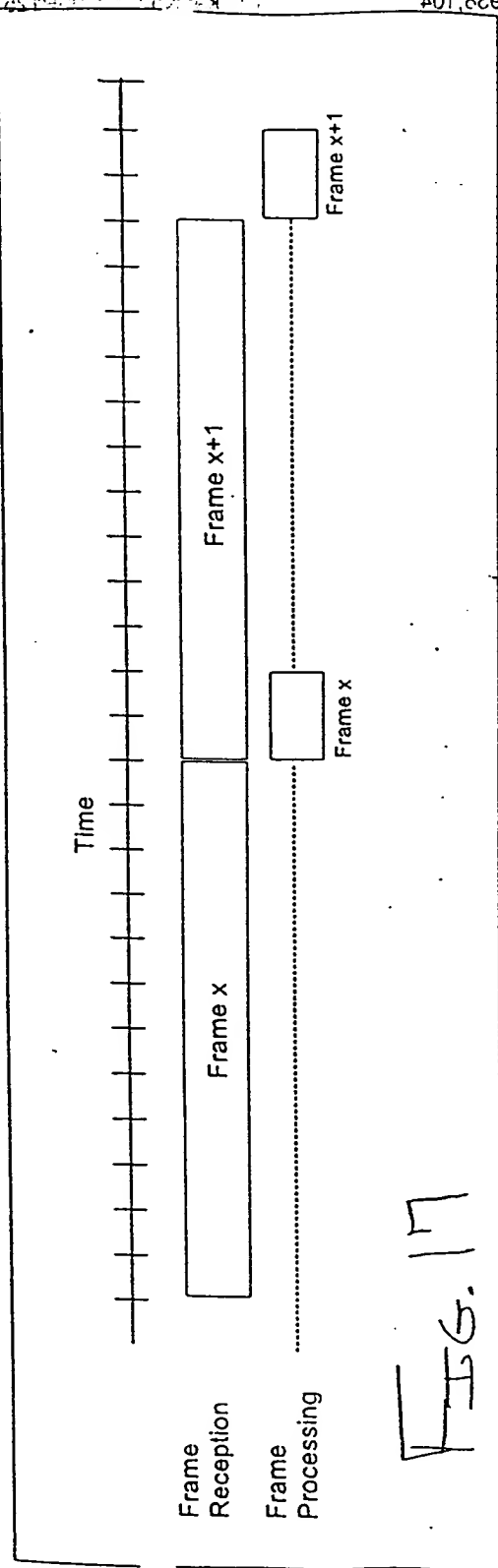


FIG. 17

